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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Stephen W. Russell Art Unit: 2815
Serial No.: 09/938,260 Examiner: Fenty, J. A.
Filing Date: 08/23/2001 Docket No.: TI-25084
Title: IMPROVED DIELECTRIC LAYER LINER FOR AN INTEGRATED
CIRCUIT STRUCTURE

REQUEST FOR EXTENSION OF TIME

FAX RECEIVED

Assistant Commissioner of Patents
Washington, DC 20231

MAR 4 2003

TECHNOLOGY CENTER 2800

Dear Sir:

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

☒ One Month (\$110)
☐ Two Months (\$410)
☐ Three Months (\$930)
☐ Four Months (\$1,450)

03/10/2003 SSURLES 00000000-200668 09928866
01 FC:1251 110.00 CH

Please charge the fee to deposit account no. 20-0668. Any further necessary extension of time is hereby requested. Charge any and all fees to deposit account no. 20-0668. An original and two copies of this sheet are enclosed.

Texas Instruments Incorporated
PO Box 655474, M/S 3999
Dallas, TX 75265
(214) 532-9348
Fax: (972) 917-4418

Respectfully Submitted,

Jacqueline J. Garner
Reg. No. 36,144